



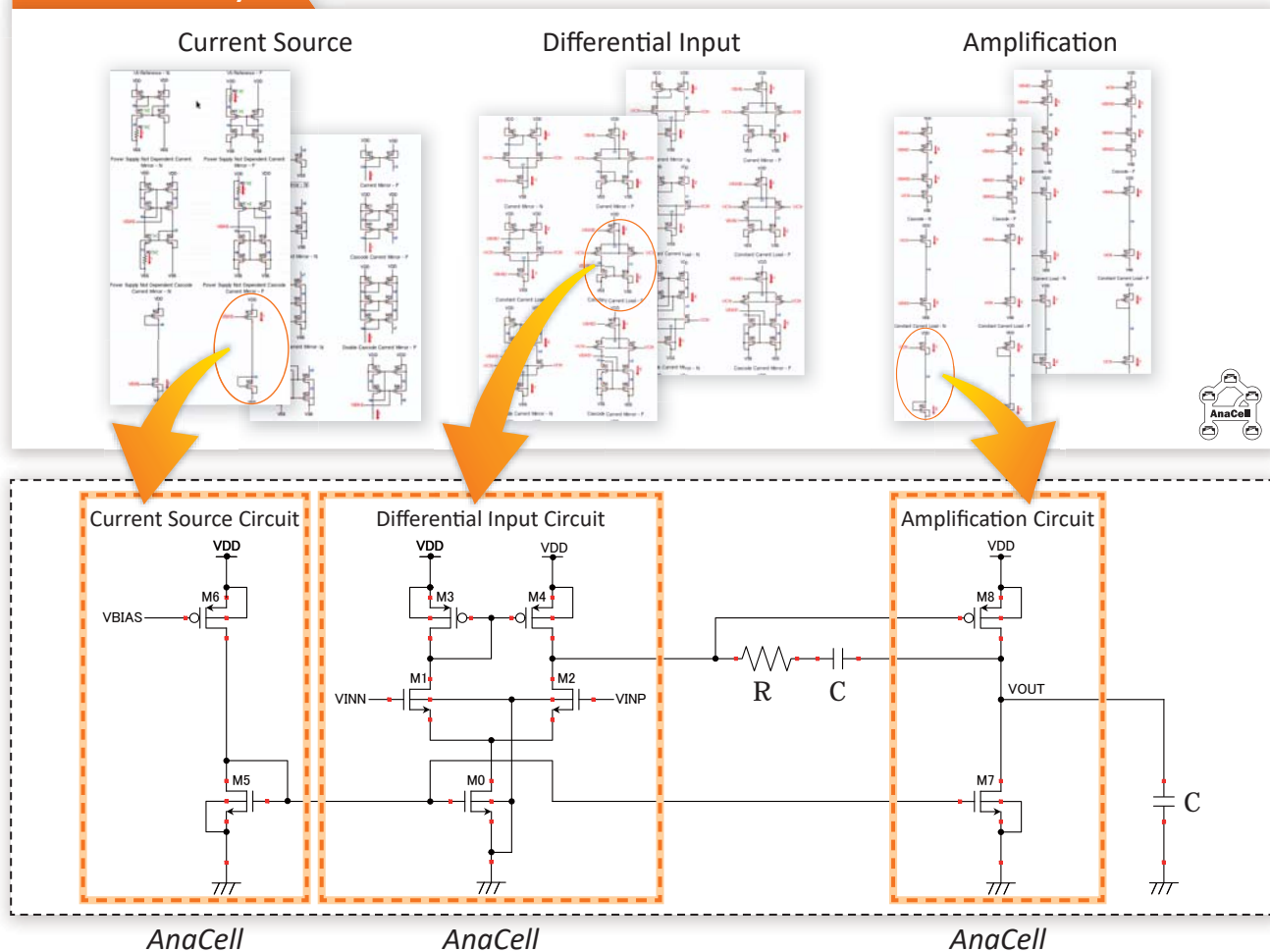
Asca-Advanced

Innovative cell-based analog circuit design
by AnaCell



Realize a method of designing an analog circuit on a cell basis
Improve design efficiency by registering general-purpose device unit circuits
as cells and assembling them quantitatively and efficiently
Promote reuse of design IPs through standardization of design

AnaCell Library

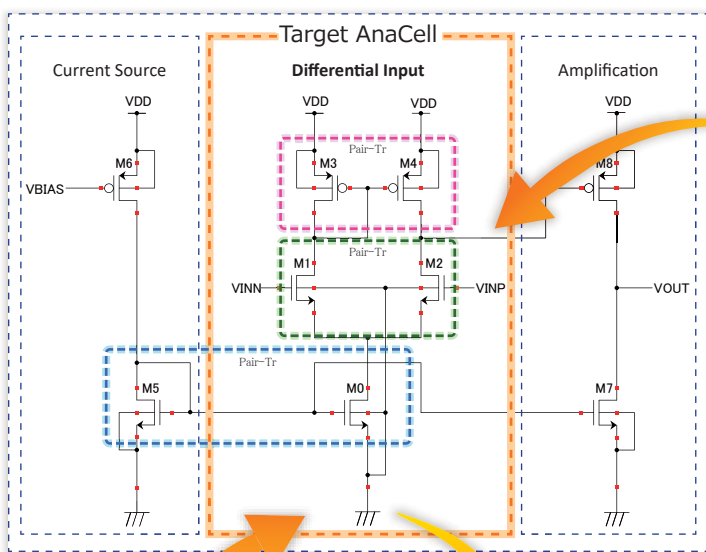


By using AnaCell* cell-based circuit design method, designers can quantitatively and efficiently perform bias design iteration, budgeting, and topology examination.

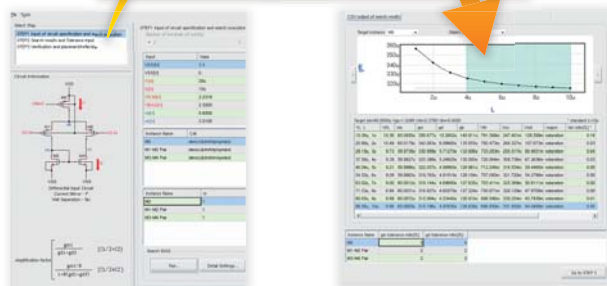
* AnaCell Patent pending

■ Asca-Advanced overview

Design features by AnaCell



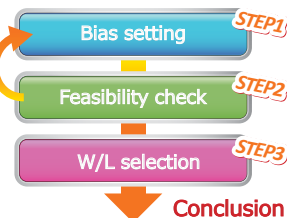
Easy operation with AnaCell Assistant



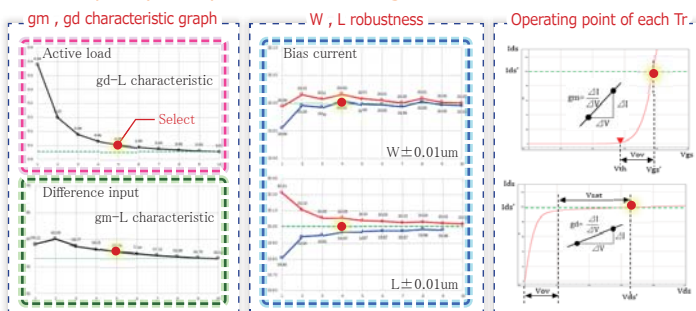
Design method standardization ,design efficiency improvement

■ DC bias input expression s.t. VICM=va

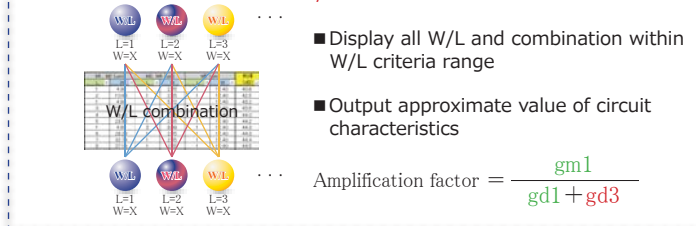
VICM	= Vds0 + Vgs1 = Vov0 + Vsat0 + Vov1 + Vthn1
VBIAS	= Vgs0 = Vov0 + Vthn0
va	= VDD - Vgs3 = VDD - (Vov3 + Vthp3)
vb	= Vds0 = Vov0 + Vsat0



Ensure quality and promote IP through visualization



W/L combination and characteristics result



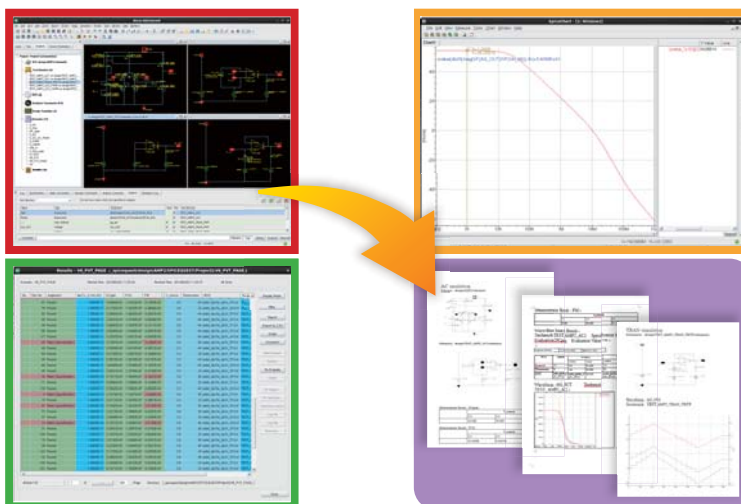
Other Features

OpenAccess
PDK support

Analysis setting
debugger

Total verification

Document
generation



Support platform

Windows/Linux

For detailed explanations, please contact the following.

※ Please note that the specifications of this product are subject to change without prior notice.

Sales and Support



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Contact

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